

UNITED STATES PATENT APPLICATION

CARBON DOPED OXIDE DEPOSITION

Inventors:

Ebrahim Andideh
Kevin L. Peterson
Jeffery D. Bielefeld

prepared by:

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
12400 Wilshire Boulevard
Seventh Floor
Los Angeles, California 90025-1026

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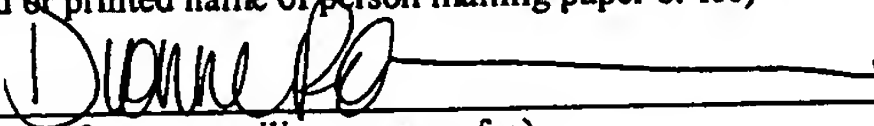
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CARBON DOPED OXIDE DEPOSITION

BACKGROUND

[0001] The present invention relates to semiconductor layer deposition. In particular, the present invention relates to carbon doped oxide deposition.

BACKGROUND OF THE RELATED ART

[0002] In the fabrication of semiconductor devices, layers of varying purposes are formed on a semiconductor substrate. One such layer, an inter-layer dielectric (ILD), is deposited and patterned to isolate and support capacitor features such as parallel conductive metal lines. As semiconductor devices and device features decrease in size, the distance between such conductive lines 275 correspondingly decreases. All other factors remaining constant, this results in a higher capacitance (C). For example, given the parallel conductive lines 275 described, capacitance (C) can be viewed as $\frac{k\epsilon A}{d}$

where (d) is the distance between the conductive lines 275, (A), the area of each conductive line interface, (ϵ), the permeability of the ILD, and (k), the dielectric constant (a factor of how much effect the ILD material has on capacitor value).

[0003] It can be seen from the above equation that, all other factors remaining constant, as the distance (d) decreases, the capacitance (C) of the system increases. Unfortunately, as capacitance (C) increases so does signal transmission time. Other problems, such as power dissipation and increased cross-talk can also occur. Therefore, reduced capacitance (C) is sought.

[0004] The dielectric constant (k) noted above has no units of measure. For example, where the dielectric is of a vacuum or air, the dielectric constant (k) is about equal to 1, having no effect on capacitance. However, most intra-layer dielectric materials have a degree of polarity with a dielectric constant (k) above 1. For example,

silicon dioxide, a common ILD material, has a dielectric constant generally exceeding about 4. Due to the decreasing size of semiconductor features (e.g. and reduced distance (d) leading to increased capacitance (C)), efforts have recently been made to reduce the dielectric constant (k) of the ILD as a means by which to reduce capacitance (C). That is, where capacitance (C) is $\frac{k\epsilon A}{d}$ and all other factors remaining constant, reduction of the dielectric constant (k) can reduce capacitance (C).

[0005] Low dielectric constant (k) materials (i.e. 'low k' materials), such as fluorinated silica glass (FSG), silk, and carbon doped oxides (CDO's) have been used to form the ILD, thereby reducing capacitance (C). However, the deposition of 'low k' materials includes a problem of low deposition rate leading to increased semiconductor processing times, also referred to as low throughput.

BRIEF DESCRIPTION OF THE DRAWINGS

[0006] Fig. 1 is a side cross-sectional view of an embodiment of a semiconductor substrate in a reactor.

[0007] Fig. 2A is a side cross-sectional view of the substrate of Fig. 1 following deposition of a CDO film.

[0008] Fig. 2B is a side cross-sectional view of the substrate of Fig. 2A following trench etch.

[0009] Fig. 2C is a side cross-sectional view of the substrate of Fig. 2B following formation of conductive metal lines.

[0010] Fig. 3 is a flowchart summarizing an embodiment of semiconductor substrate processing.

DETAILED DESCRIPTION

[0011] Methods of deposition of CDO features are described. Aspects of embodiments are described and illustrated by the accompanying drawings. While the

following embodiments are described with reference to deposition of particular carbon doped oxide films to form ILD's, the embodiments are applicable to the formation of any carbon doped oxide feature. This can include carbon doped oxide films formed from precursors having formulas such as $H_xSi(CH_3)_{4-x}$ and $(CH_3)_xSi(OCH_3)_{4-x}$, or tetramethylcyclotetrasiloxane, among others.

[0012] Embodiments described below are generally applicable to processing of a semiconductor substrate. Once a substrate has been obtained, initial processing can include the formation of a dielectric layer above a surface of the substrate. Embodiments described here focus on the deposition of CDO dielectric material, in particular, by introduction of oxygen in the presence of a CDO precursor to increase the rate of CDO deposition.

[0013] Referring to Fig. 1, an embodiment is shown of a substrate 100 that is exposed to a gas mixture 160 that includes a CDO precursor. Oxygen is also included in the gas mixture 160 to increase the rate of CDO deposition on the substrate 100 when, for example, the gas mixture 160 is excited to cause deposition as described further herein.

[0014] The substrate 100 includes an initial dielectric etch-stop layer 120. The substrate 100 can be of silicon and other conventional materials. The etch-stop layer 120 is not required but is shown for illustration as described further herein. Additionally, in other embodiments of the invention various other insulating or conducting layers and features may be present on the substrate 100 that are not necessarily described here.

[0015] The substrate 100 is placed within a reactor 180 for deposition of a material on the substrate 100. In the embodiment shown, the material to be formed on the substrate 100 is a Carbon Doped Oxide (CDO) to act as an ILD. A CDO material is a

material that has incorporated silicon (Si), carbon (C), and oxygen (O), providing relatively reduced polarity, density, and conductivity. For example, CDO material can have a dielectric constant (k) that is less than about 3.0. Therefore, CDO materials are generally considered 'low k' materials, and useful for lowering capacitance when used as ILD's as discussed further herein.

[0016] In one embodiment, the reactor 180 is a conventional chemical vapor deposition (CVD) apparatus. The CVD apparatus may be plasma enhanced (i.e. a PECVD apparatus) and operated by conventional means. In the embodiment shown, the PECVD apparatus is provided with a shower plate 150 coupled to a power source 155. The substrate 100 is grounded and positioned adjacent the shower plate 150. In the embodiment shown, the substrate 100 is positioned between about 15mm and about 40mm from the shower plate 150, preferably between about 24mm and about 26mm.

[0017] Once the substrate 100 is positioned and the PECVD apparatus sealed, the gas mixture 160 is introduced into the apparatus in vapor form. Simultaneously, radio frequency (RF) is applied through the PECVD apparatus such that the gas mixture 160 is excited to a plasma state to cause deposition on the surface of the substrate 100. In alternate embodiments, at least a portion of the gas mixture 160 is excited to a plasma state at a remote location prior to introduction to the PECVD apparatus. In such an embodiment, this portion of the gas mixture 160 can enter the PECVD apparatus already in a plasma state.

[0018] The gas mixture 160 referenced above includes a CDO precursor and a deposition enhancing gas such as oxygen. As discussed further herein, the inclusion of oxygen increases the rate of deposition and formation of the CDO material. An inert gas such as helium (He) can also be provided as part of the gas mixture 160 to serve as a volume-filler within the PECVD apparatus during deposition. The inert gas can also

be a good heat conductor to promote thermal uniformity. However, it does not actually take part, chemically, in the deposition process. In addition to helium (He), other inert gasses, such as argon (Ar), neon (Ne), krypton (Kr), and xenon (Xe) can be used as a background gas.

[0019] A CDO precursor, which is a part of the gas mixture 160 described above, supplies a source of carbon (C) and silicon (Si) for the formation of CDO features. A CDO precursor can also supply a source of oxygen (O). However, this is not required, as oxygen (O) is separately provided according to embodiments described herein. Embodiments of CDO precursors include tetramethylcyclotetrasiloxane $(\text{HSiOCH}_3)_4$ and gasses having a formula of $\text{H}_x\text{Si}(\text{CH}_3)_{4-x}$ or $(\text{CH}_3)_x\text{Si}(\text{OCH}_3)_{4-x}$. For example, in one embodiment dimethyldimethoxysilane $(\text{CH}_3)_2\text{Si}(\text{OCH}_3)_2$ is used as the CDO precursor. Such CDO precursors result in a CDO material with a dielectric constant (k) that is less than about 3.0 as described above.

[0020] In the embodiment described above, oxygen gas is supplied to the PECVD apparatus and excited (e.g. O^*) by application of RF. The oxygen gas supplied as part of the gas mixture 160 can initially be in the form of ionic oxygen (e.g. O^{2-}), molecularly stable oxygen (O_2), elementally stable oxygen (O), or ozone (O_3). Regardless, RF is applied affecting the oxygen gas as it enters the PECVD apparatus such that at least a portion of the oxygen molecules will be in an excited state (e.g. O^*). The excited oxygen molecules interact with the CDO precursor to increase the rate of CDO deposition. In another embodiment, oxygen, in the form of ozone (O_3) is excited thermally without use of RF to increase the rate of CDO deposition. The ozone (O_3) form of the oxygen gas is more readily excitable in this manner.

[0021] In another alternate embodiment, the oxygen gas is excited, along with the background gas, at a remote location separate from the CDO precursor of the gas

mixture 160. In this embodiment, the CDO precursor is excited upon introduction to the PECVD apparatus, where it combines with the already excited oxygen gas.

[0022] As described above, the gas mixture 160 enters the PECVD apparatus, which is energized by introduction of RF. The process can proceed within the PECVD apparatus at conventional pressures, temperatures, radio frequency (RF) and power. For example, in one embodiment, pressure is maintained between about 2.0 Torr and about 10.0 Torr, preferably between about 3.0 Torr and about 6.0 Torr, temperature of the susceptor 135 supporting the substrate 100 is maintained between about 250°C and about 450°C, and RF is maintained at standard frequencies and between about 1,600 watts and about 1,800 watts are supplied.

[0023] With respect to the embodiments described above, the oxygen gas is in an amount that is less than about 5% of the volume taken up by the oxygen and CDO precursor. Additionally, flow rates, in standard cubic centimeters per minute (Sccm), for the individual gasses of the gas mixture 160 can be as follows:

Precursor gas flow rate	50-200 Sccm
Background gas flow rate	20-200 Sccm
Oxygen gas flow rate	1.0-20 Sccm

[0024] Flow rates such as those above can be established depending on a variety of factors, such as temperature and pressure conditions. In fact, flow rates other than those described above can be used without departing from the spirit and scope of the present invention.

[0025] The addition of oxygen to the gas mixture 160 in a manner such as that described above leads to a deposition rate of CDO onto the substrate 100 that can exceed about 5,620 angstroms per minute. In one embodiment, CDO deposition rate is

between about 5,620 angstroms per minute and about 9,600 angstroms per minute, preferably up to about 9,580 angstroms per minute. This can translate into about a 70% increase in CDO deposition rate as compared to conventional PECVD deposition of CDO without the introduction of oxygen.

[0026] Referring to Fig. 2A, the substrate 100 of the embodiment of Fig. 1 is shown with a CDO film 200 deposited above an etch-stop layer 120. The CDO film 200 has a dielectric constant that is less than about 3.0. In one embodiment the CDO film dielectric constant is less than about 2.7. Additionally, deposition of the CDO film 200 in the presence of oxygen may provide a slightly lower dielectric constant, when compared to the deposition of the same CDO film 200 in the absence of oxygen. In the embodiment shown the CDO film 200 is to form a CDO ILD (see Fig. 3). However this is not required. The CDO film 200 can be used for a variety of insulating purposes.

[0027] Referring to Figure 2B, the CDO film 200 is etched to form trenches 250. In the embodiment shown, the CDO film 200 is patterned and etched by conventional means. For example, a protective mask pattern can be placed above the CDO film 200 exposing areas to form the parallel trenches 250. A chemical etchant is then applied to etch through exposed portions of the CDO film 200. The etch-stop layer 120 is of a material resistant to chemical etchant and helps control the depth of the etched trenches 250. The etch-stop layer 120 can be of silicon nitride (SiN), silicon carbide (SiC), or other conventional etch-stop material.

[0028] Referring to Fig. 2C, the deposited CDO film 200 provides structural support and isolation of conductive lines 275 in the form of a CDO ILD. In the embodiment shown, the conductive lines 275 are deposited upon etch of the CDO film 200 to form a CDO ILD down to the etch-stop layer 120. In one embodiment, the conductive lines 275 are of copper (Cu). Additionally, in one embodiment the etch-

stop layer 120 also acts as a barrier to prevent diffusion of copper ions (Cu^+) to below the etch-stop layer 120, therefore maintaining a degree of isolation of the conductive lines 275.

[0029] The conductive lines 275 can be deposited by conventional means. For example, in one embodiment, an ionized form of conductive line material (e.g. Cu^+) can be supplied in vapor form to a conventional PECVD apparatus. RF can be applied to the apparatus to generate a plasma and effect deposition of a conductive layer including the conductive lines 275. Other excess of the conductive layer can be removed by conventional Chemical-Mechanical Polishing (CMP) techniques such that the substrate 100 includes a smooth upper surface 290 and further isolating the conductive lines 275 as shown in Fig. 2C.

[0030] The deposited conductive lines 275 are separated by a distance (d) wherein ILD material 200 is present isolating the conductive lines 275. As described earlier, where capacitance (C) is $\frac{k\epsilon A}{d}$, reduction in the distance (d) can increase capacitance (C). However, embodiments described here include the use of 'low k' CDO ILD material 200 to compensate for this problem in a manner that does not sacrifice reasonable throughput (e.g. semiconductor processing time) due to lengthy CDO deposition times.

[0031] Referring to Fig. 3, a summary of a preferred embodiment of CDO deposition according to the methods described above is shown in the form of a flowchart. In embodiments described here, a substrate is placed in a reactor where a CDO precursor and oxygen are introduced 310. Deposition results to form a CDO film on the substrate. The deposition takes place in an otherwise conventional manner, such as within a PECVD apparatus operated under conventional conditions. Deposition in this manner takes place at an increased rate due to the presence of oxygen. The CDO

film is then etched 320. Etching 320 of the CDO is achieved by conventional methods, with conventional etchants applied to the CDO film. Once etching 320 is complete, conductive lines 275 are deposited 330, again by conventional means such as within a PECVD apparatus operated under conventional conditions. CMP 340 is then applied and the substrate is available for completion of further processing and packaging 350.

[0032] Embodiments described above include CDO deposition in the presence of oxygen. Additionally, embodiments include reference to particular 'low k' materials deposited to form ILD's. Although exemplary embodiments describe particular CDO materials deposited to form ILD's, additional embodiments are possible. For example, CDO films can be formed at an increased rate according to embodiments discussed above for insulating purposes apart from ILD formation. Furthermore, many changes, modifications, and substitutions may be made without departing from the spirit and scope of these embodiments.